EXCEED
trustEd and fleXible system-on-Chip
for EuropEan Defence applications

gives the floor to...

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WHAT IS YOUR BACKGROUND?

I received a **MSc in Electronic Engineering** and a PhD (VLSI course) from the **University of Rome "Tor Vergata"** in 2008 and 2012, respectively. I worked on computer arithmetic and VLSI designs for low-power digital signal processing targeting ASIC/FPGA devices as a researcher.
I joined **Elettronica (ELT)** in Rome since 2013, initially as a FPGA Designer and now as a system engineer participating in the development of modern digital systems for Electronic Warfare.

WHAT IS THE ROLE OF YOUR ORGANIZATION IN THE PROJECT? WHAT IS YOUR ROLE?

ELT is involved in the EXCEED project at different levels and with different roles:
1. **WP leader in Work Package 4**: technical, organizational and administrative activities in WP4 with the responsibility to define the **EXCEED SoC/SiP architectures** together with the technical partners involved (e.g., architectural deliverables issues and one project milestone achievement).
2. Active contributor in WP3/WP7: **general SoC/SiP requirements collection activity** (WP3) and **use case implementation** (WP7).
3. Minor contributor in WP2/WP8: general security requirements collection activities (WP2) and project exploitation & dissemination activities (WP8).
My responsibility is to support these different roles (leading WP4 activities, in particular)

WP: Work Package| SoC: System-on-Chip | SiP: System-in-package

WHAT ARE THE EXPECTED ACHIEVEMENTS FOR YOUR WP?

The main aim of WP4 is to derive the **EXCEED SoC/SiP detailed reference design architecture** by analysing the requirements collected in WP2/WP3 (based on defence applications needs) against the SoC/SiP state of the art and manufacturing constraints. This is an important milestone of the project.
In WP4 we also derive: links between requirements and architectural building blocks, the test tool environment architecture to support the test plan derived in WP3.

WHAT WILL EXCEED BRING TO YOU AND YOUR ORGANISATION?

The EXCEED project represents a great opportunity for ELT. **While the consortium is aiming to create a trusted semiconductor supply chain of sovereign European technology**, ELT has the possibility to specify the **SoC features according to its own needs and experience** in the defence market incrementing the competiveness of its products, on a long term perspective.
Moreover, the team created to deliver the project results is composed of partners who are experts in different fields of engineering. **In this context, ELT (and I) has the opportunity to improve its knowledge by solving the challenges of the project together**.